

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,110,125 B1
APPLICATION NO. : 09/585607
DATED : September 19, 2006
INVENTOR(S) : Takashi Ono et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

ON TITLE PAGE AT (30) FOREIGN APPLICATION PRIORITY DATA

“11/180005” should read --11-18005--.

COLUMN 3

Line 8, “analyzer” should read --an analyzer--; and
Line 24, “processing-terminal.” should read --processing terminal.--.

COLUMN 7

Line 66, “buffer memory 37” should read --buffer memory 38--.

Signed and Sealed this

Eighth Day of April, 2008

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS
Director of the United States Patent and Trademark Office